the data coder when the data read output signals do not have the predetermined values, the output stage producing a relatively high impedance at the data bit line when the data read output signals have the predetermined values.

- 76. The computer system of claim 75 wherein the predetermined values of the data read output signals are any values in which the data read output signals at the respective data output terminals of the data coder have the same value.
- 77. The computer system of claim 76 wherein the predetermined values of the data read output signal correspond to logic "1".
- 78. The computer system of claim 75 wherein the output stage comprises a logic circuit that causes the output data bit to have a first logic level responsive to one of the data read output signals having a first predetermined logic level, that causes the output data bit to have a second logic level responsive to the other of the data read output signals having a second predetermined logic level, and that causes the data bit line to have the relatively high impedance responsive to both of the read output signals having other than the first and second predetermined logic levels. --

## REMARKS

Consideration of this reissue application in view of the above amendments and following remarks is respectfully requested. Applicant is requesting reissue on the basis that the '314 patent is, through error without any deceptive intention, deemed wholly or partially inoperative by reason of the patentee claiming less than he had a right to claim in the '314 patent. The assignee offers to surrender the '314 patent once the Examiner has indicated the allowability of all claims associated with this reissue application. The assignee is requesting a reissue of the '314 patent for the invention as disclosed in the '314 patent and claimed in issued claims 1-53 and new claims 54-78.

No new matter has been introduced into this application for reissue.

Applicant believes claims 1-78 are in condition for allowance and respectfully requests such action.

If there are any matters that can be handled in a telephone conversation, the Examiner is encouraged to contact Mr. Bulchis at (206) 903-8785.

Respectfully submitted,

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